# Microprocessor

SYBCA Unit Test-1

* Required

1. Full Name *

2. Class *

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- SYBCA-1
- SYBCA-2

3. Roll Number *

Attempt all the questions

Each question carry 1 mark.

4. 1. Serial I/O port in 8085 MPU is_____ *

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- SID & SOD
- Parallel
- synchronous
- asynchronous

5. 2. The word length of the 8085 microprocessor is _____ *

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- 8 bit
- 4 bit
- 16 bit
- 32 bit

6. 3. TTL stands for: *

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- Translator to Translator Logic
- Transistor to Transistor Lines
- Transistor to Transistor Logic
- Translator to Translator Lines

7. 4. There are _____ instructions in instruction set of Intel 8085

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- 74
- 47
- 84
- 87

8. 5. Which is the incorrect arithmetic and logical operation of 8085 *

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- 8 bit binary addition
- 16 bit binary addition
- 8 bit logical AND, OR, EXOR and complements
- 16 bit rotation

9. 6. The architecture of 8085 is accumulator based. It provide: *

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- one accumulator, one flag register, six general purpose registers and two special purpose registers
- two accumulator, one flag register, six general purpose registers and two special purpose registers
- one accumulator, one flag register, six general purpose registers and one special purpose registers
- two accumulator, one flag register, six general purpose registers and one special purpose registers

10. 7. Which statement is incurrect about Integrated Circuit. *

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- IC can work on low voltage
- It can handle limited amount of power
- Acquire large space
- Complex circuit on chip may be used to obtain improved performance characteristic

11. 8. Which one is not a phase in the execution process *

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- Fetch
- Decode
- Execute
- process

12. 9. The logic devices of microprocessor are: *

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- ALU, Several Registers, Control Unit
- ALU, Several Registers, Logic Unit
- ALU, Fetch Unit, Control Unit
- ALU, Decoder, Control Unit

13. 10. Output of the counter is given to the_____ *

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- Decoder
- Register
- ALU
- Control switch

14. 11. The data which decide operation of microprocessor is called *

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- Pseudocode
- Datacode
- Opcode
- Mnemoniccode

15. 12. Microprocessor consist following *

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- Counter
- ALU and Registers
- Control unit
- All of the above

16. 13. Memory is combination of *

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- Decoder and control switch
- Control switch and control circuit
- Register array and decoder
- ALU and register

17. 14. ALU perform which operation *

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- Arithmetic
- Logical
- Both a and b
- None of the above

18. 15. ALU uses which 2 registers as input *

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- Reg a and Reg B
- Accumulator or Temp Reg
- Reg b and Temp Reg
- None of the above

19. 16. ALU works on how many data words *

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- 1
- 2
- Both a and b
- None of the above

20. 17. After operation ALU save its output in which register *

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- Reg A
- Data bus
- Control circuit
- Reg B

21. 18. Use of accumulator is/are *

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- To provide one of the operands for ALU operation
- Data transfer between IO port and a memory
- To save the output of ALU operation
- All of the above

22. 19. Flag register is also called as *

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- Reg a
- Reg b
- Status reg
- None of the above

23. 20. which of the pair is used in general purpose register *

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- BH
- CD
- AE
- HL

24. 21. In which register opcode is store before execution *

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- Reg A
- Accumulator
- Instruction reg
- Program counter

25. 22. Temporary register is used for____ *

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- To provide the input for ALU
- To store the output of ALU
- Both a and b
- None of the above

26. 23. Control logic perform *

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- Generate control signals to carry out instruction
- Process interruption
- Look after microprocessor power up sequence
- All of the above

27. 24. Following is not register in 8085 microprocessor *

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- Register B
- Register D
- Register E
- Register P

28. 25. This active high signal is used to acknowledge HOLD request *

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- HOLD
- HLDA